

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (Cancelled).
2. (Currently amended) ~~Transmitting device (1) according to claim 1~~ A transmitting device for a multipoint-to-point network, wherein the multipoint-to-point network is a synchronous multipoint-to-point CDMA network, containing a first unit for generating a coded communications signal, in particular a CDMA-coded communications signal, and a second unit for generating a coded synchronization signal, the second unit is used for generating a synchronization signal with a signal level which is lower than a signal level of the communications signal and/or for coding the synchronization signal with a code which differs from a code of the communications signal, and wherein a modulator is provided, which is connected in series to the second unit and is used to modulate the coded synchronization signal, in particular using alternating multiplication by +1 and -1, characterized in that the synchronization signal is sent in the same transmission channel and/or in the same frequency range as the communications signal.
3. (Cancelled).

4. (Currently amended) ~~Receiving device (3)~~ according to claim 3 ~~A receiving device~~ for a multipoint-to-point network, wherein the multipoint-to-point network is a synchronous multipoint-to-point CDMA network, containing a first unit for receiving and detecting a communications signal, in particular a CDMA-coded communications signal, and a second unit for receiving and detecting a coded synchronization signal, wherein the second unit contains a series circuit of a demodulator and a logical correlator and is used for demodulating and detecting the coded synchronization signal, said coded synchronization signal having a signal level which is lower than a signal level of the communications signal and/or is coded using a code which differs from a code of the communications signal, characterized in that the second unit (19) contains two logical correlators (13, 16) connected in parallel for correlating the synchronization signal and two demodulators (12, 15), that ~~one~~ a first demodulator (15) is connected in ~~the~~ an incoming circuit to ~~one~~ a first correlator (16) and ~~the other~~ a second demodulator (12) is connected in an incoming circuit to ~~the other~~ a second correlator (13), that ~~one~~ the first demodulator (15) is driven by a first clock pulse and that the second demodulator (12) is driven by a second clock pulse, the second clock pulse having the same clock pulse frequency as the first clock pulse and a preset phase difference compared with the first clock pulse.

5. (Currently amended) A ~~R~~receiving device ~~(3)~~ according to claim 4, characterized in that a delay element ~~(17)~~ with a delay of half a clock pulse period is provided, ~~which said delay~~ element is used for generating the second clock pulse from the first clock pulse.

6. (Currently amended) A ~~R~~receiving device ~~(3)~~ according to claim 4, characterized in that each demodulator ~~(12, 15)~~ is suitable for carrying out demodulation using alternating multiplication by +1 and -1.

7. (Currently amended) A ~~R~~receiving device ~~(3)~~ according to claim 4, characterized in that a selector switch ~~(14)~~ for selecting one of the outputs of the correlators ~~(13, 16)~~ is connected in series to the correlators ~~(13, 16)~~.

8. (Currently amended) A ~~R~~receiving device ~~(3)~~ according to claim 4, characterized in that the first clock pulse corresponds to ~~the~~ a symbol clock pulse of a coded communications signal.

9. (Currently amended) A ~~R~~receiving device ~~(3)~~ according to claim ~~3~~ 4, characterized in that the demodulator ~~(20)~~ is driven by a clock pulse in which at least two phase positions can be set.

10. (Cancelled).